TRADEMA"



AMEN	NDMENT T	ΓRANSMI	TTAL LE	TTER	Docket No. R2184.0247/P2
Applicatio		Filing Date		Examiner	
10/633,681-Conf. #6878		August 5, 2003		E. J. Wojciecho	vicz 2815
plicant(s): Keii					
vention: SEMIC		NTEGRATED	CIRCUIT DE	VICE AND FABRIC	CATION METHOD
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ransmitted here	with is an ame	ndment in the	above-identif	ied application.	
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	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
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(202) 420-4742



Docket No.: R2184.0247/P247

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Keiichi Yoshioka

Application No.: 10/633,681

Confirmation No.: 6878

Filed: August 5, 2003

Art Unit: 2815

For: SEMICONDUCTOR INTEGRATED

CIRCUIT DEVICE AND FABRICATION

METHOD THEREOF

Examiner: E. J. Wojciechowicz

RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated October 10, 2006, please amend the above-identified U.S. patent application as follows:

A Listing of the Claims begins on page 2 of this paper.

Remarks begin on page 7 of this paper.